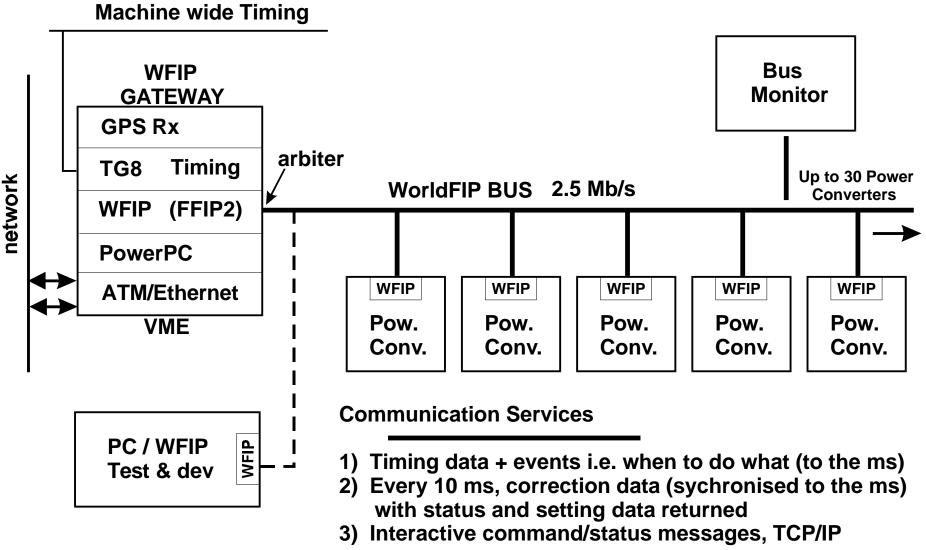
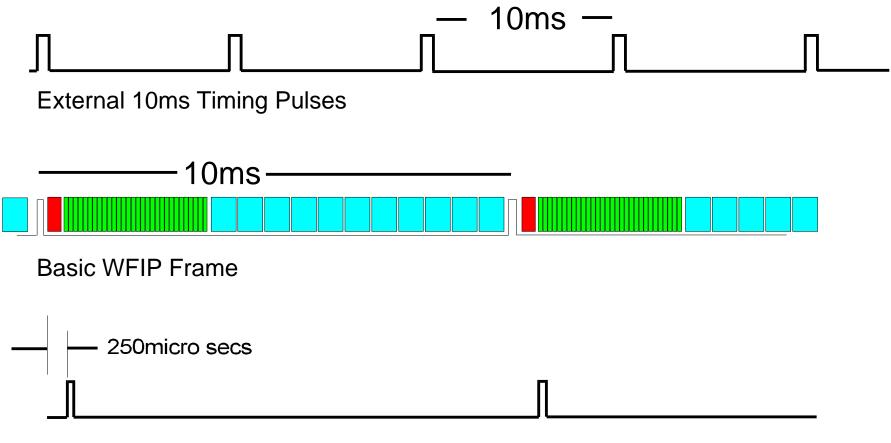
Proposal for LHC/SM18 Power Converter Communication Structure, using WorldFIP Fieldbus



WorldFIP3 - JGP - 4/12/98

Basic WFIP Timing for Power Converter Communication



Interrupt to HC16 Micro-controller [used to synch 1ms clk]

WorldFIP4 8/12/98